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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

023925-00004

SERIAL NO.

09/863,318

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

FAN et al.

FILING DATE

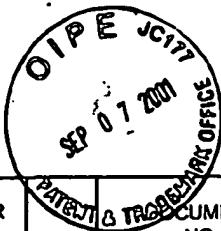
May 24, 2001

GROUP

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HKC	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
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	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
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	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
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11/11	BE 5,918,074	Jun 29, 1999	Wright et al.			Jul. 25, 1997
	BF 5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG 5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
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	BI 5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
	BJ 5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
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	BP	EP0465090A1	Jan. 8, 1992	EPO					
	BQ	EP0859492A2	Aug. 19, 1998	EPO					
	BR	EP0312917A2	Apr. 26, 1989	EPO					
	BS	EP0853441A2	Jul. 15, 1998	EPO					
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	BY	EP0854606A2	Jul. 22, 1998	EPO					
	BZ	WO 99/00948	Jan. 7, 1999	WIPO					
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	CB	WO 99/00950	Jan. 7, 1999	WIPO					
	CC	WO 99/00939	Jan. 7, 1999	WIPO					
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	CF	WO 99/00945	Jan. 7, 1999	WIPO					
	CG	EP0849917A2	Jun. 24, 1998	EPO					
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ilke	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
ilke	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
ilke	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
ilke	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
ilke	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER <i>Kenneth R. Gault</i>	DATE CONSIDERED <i>9/18/04</i>
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	